

FIGURE 1



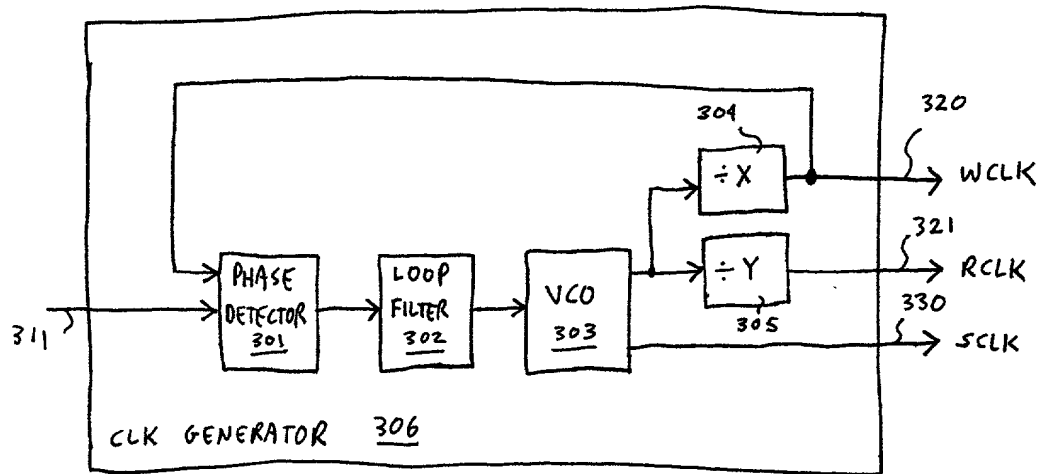


FIGURE 3

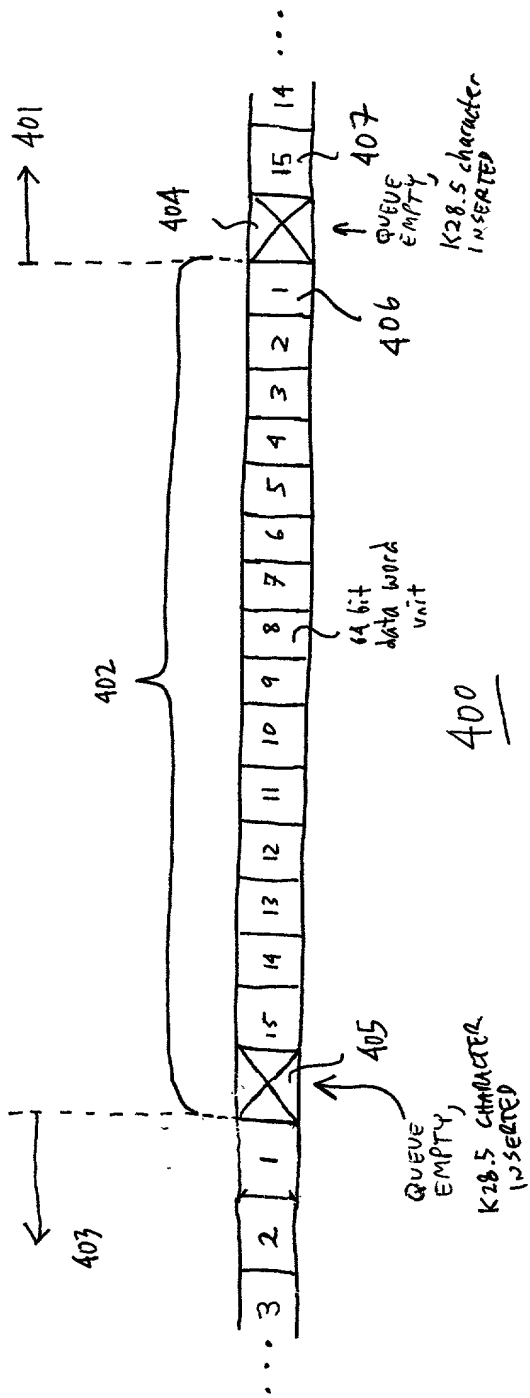


FIGURE 4

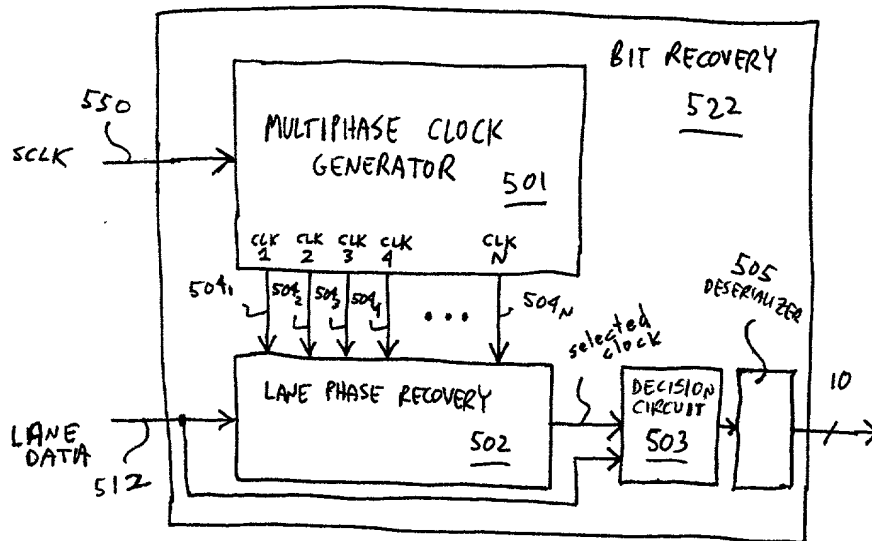


FIG. 5A

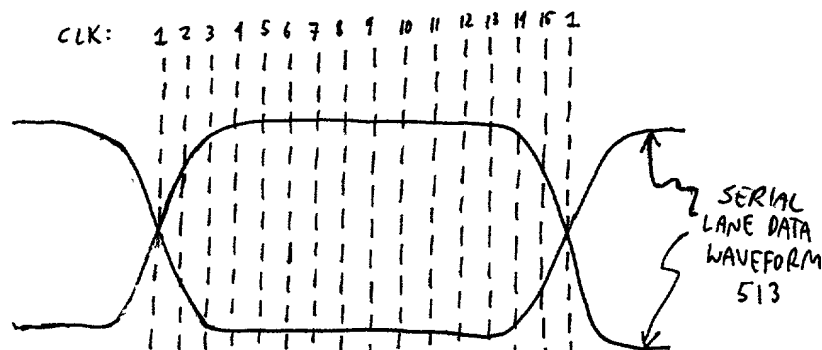


FIG. 5B

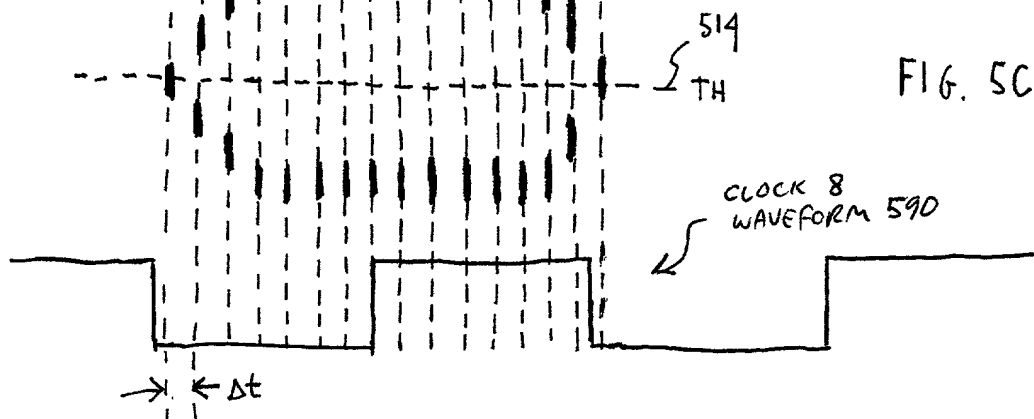


FIG. 5C

FIG. 6 is a block diagram of a data alignment unit 600. The unit 600 includes a data alignment data structure detect block 603, a rotating multiplexer (MUX) 604, and two registers, REG. 1 601 and REG. 2 602. The unit 600 is connected to a FROM BIT RECOVERY UNIT 606 and a 10-bit bus 607. The unit 600 is also connected to a 10-bit bus 605.

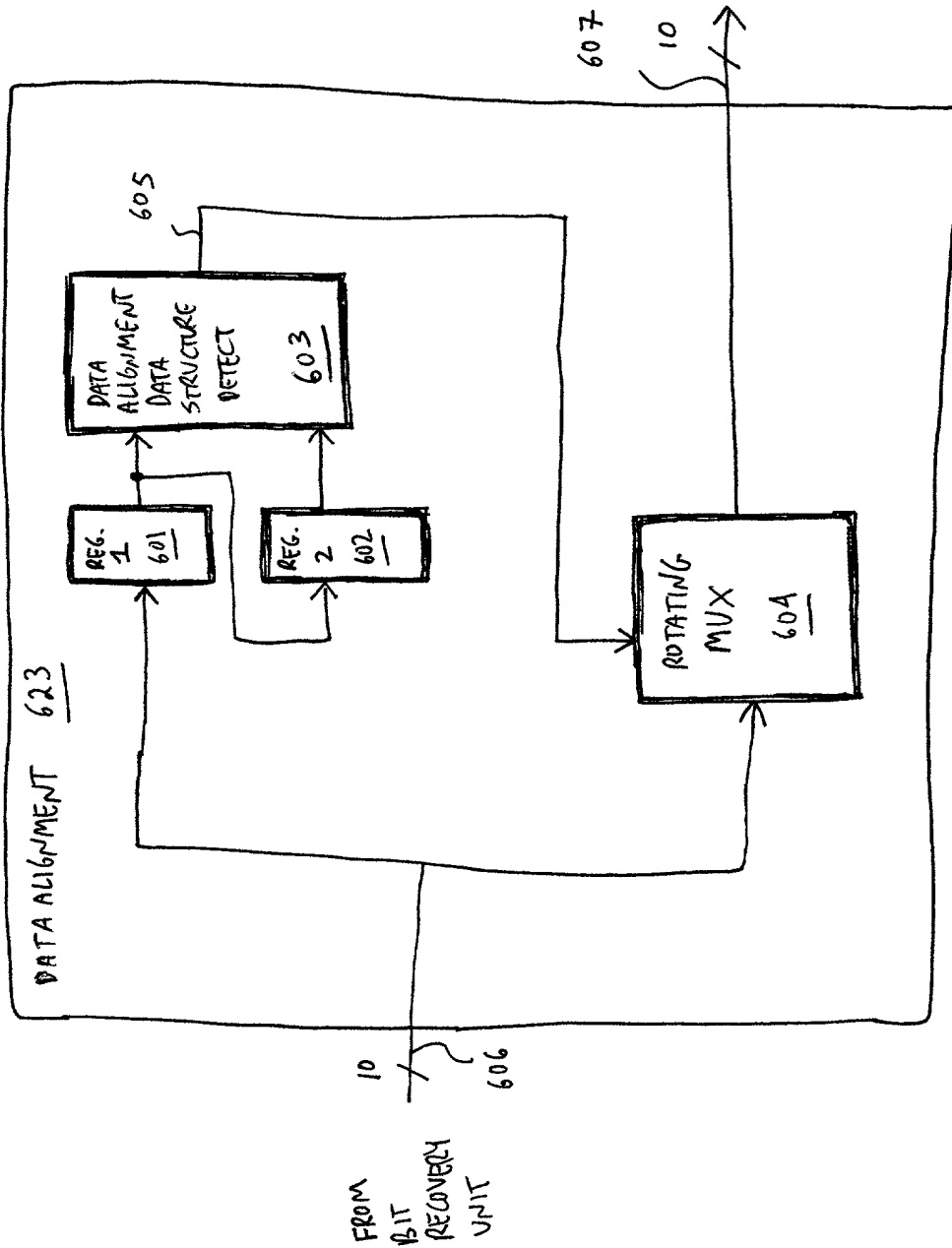


FIGURE 6

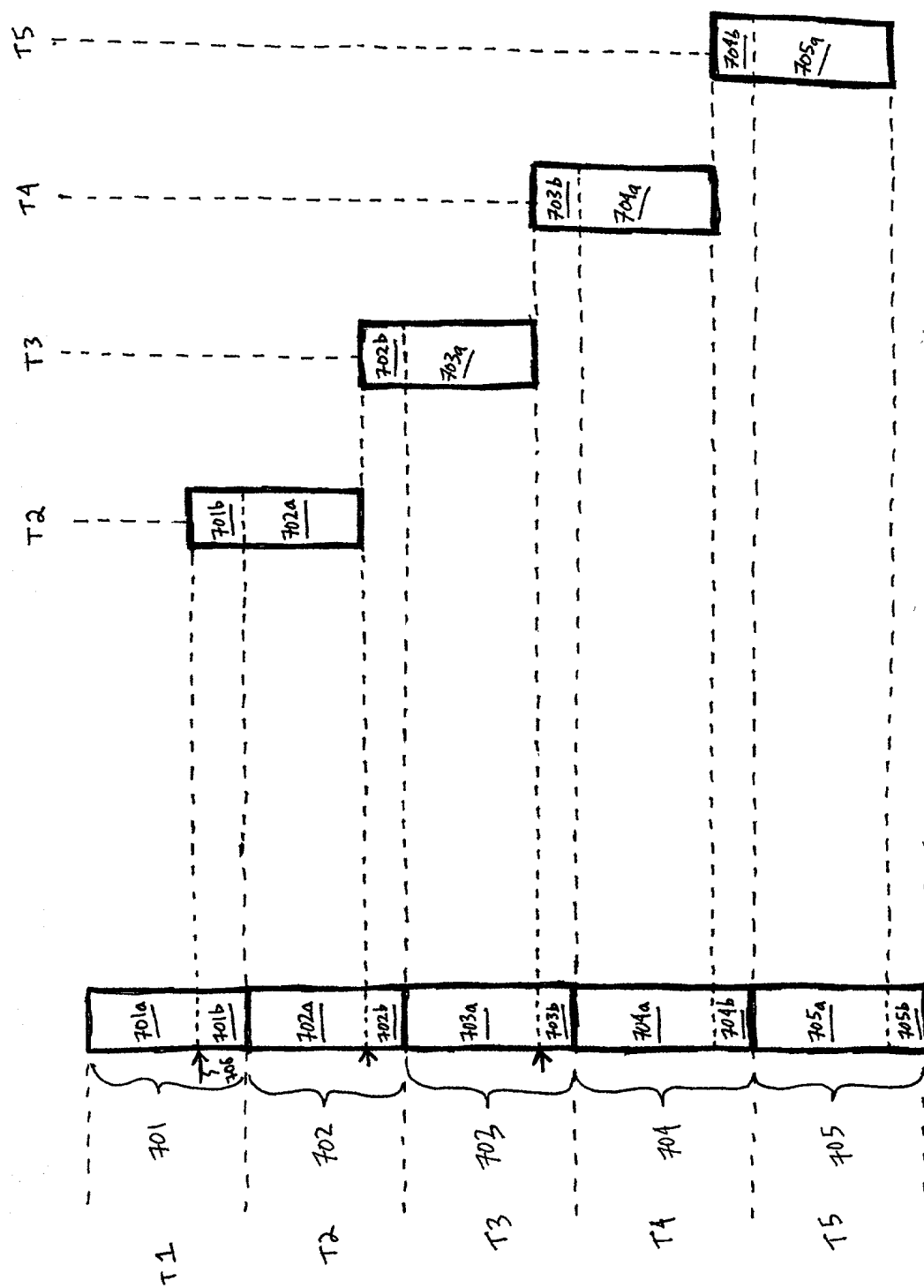


FIG. 7B

FIG. 7A

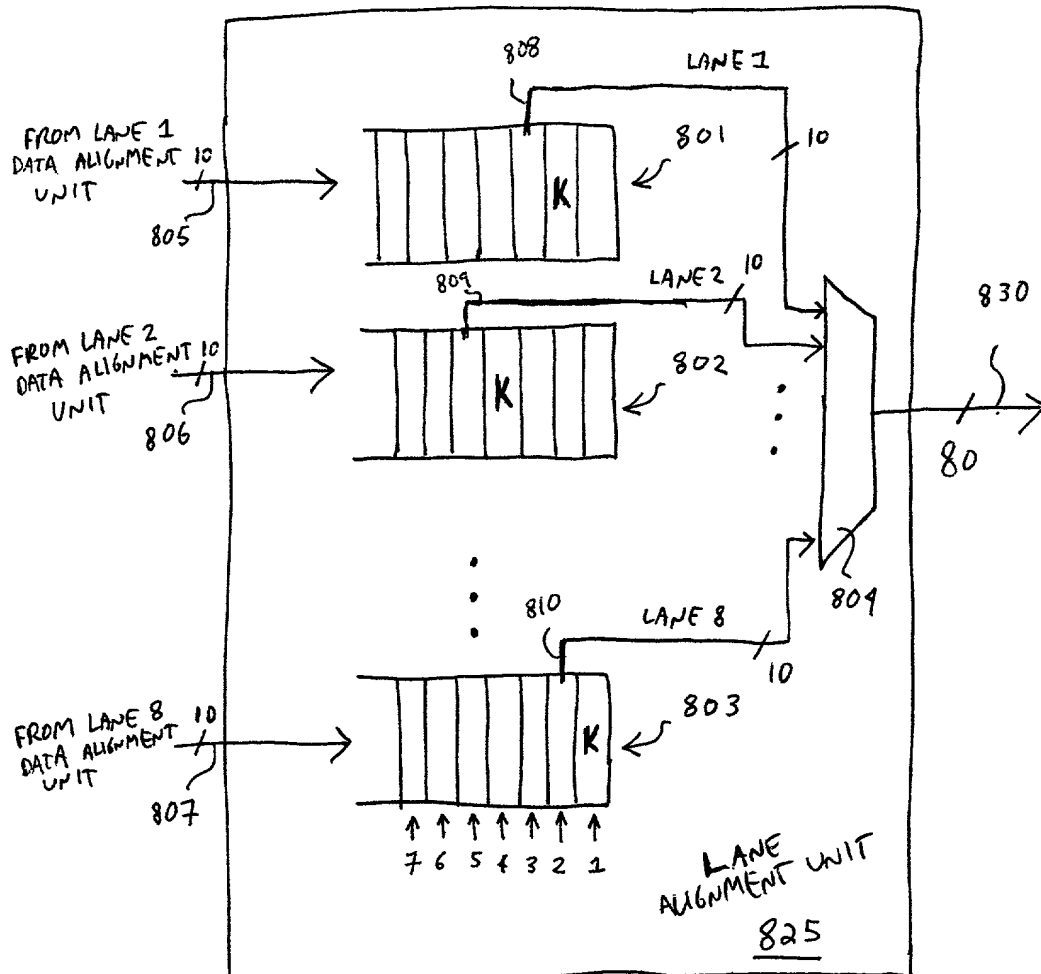


FIGURE 8